

10511 U.S. PTO  
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	Subclass	ISSUE CLASSIFICATION
	Class	

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PATENT NUMBER

## U.S. UTILITY Patent Application

(2)	O.I.P.E. <i>H5</i>	PATENT DATE
SCANNED <i>AA</i>	Q.A. <i>OMW</i>	

APPLICATION NO. 09/506033	CONTINUED D F	CLASS 257 374-5	SUBCLASS 193	ART UNIT 211 2674	EXAMINER <i>FATIMA H. TAN</i> <i>S. S. LEE</i>
APPLICANTS Kazuyuki Kubota Hidetomo Matsuda Toshiro Shiraki Naotaka Maeda					
TITLE Latch circuit, shift register circuit, logical circuit and image display device operated with a low consumption of power					
PICKONE TECHNOLOGY					

ISSUING CLASSIFICATION					
ORIGINAL		CROSS REFERENCE(S)			
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)		
INTERNATIONAL CLASSIFICATION					
Continued on Issue Slip Inside File Jacket					

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	(Assistant Examiner) _____ (Date) _____			NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent No. _____	(Primary Examiner) _____ (Date) _____			ISSUE FEE	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	(Legal Instruments Examiner) _____ (Date) _____			ISSUE BATCH NUMBER	

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